Chip Creator™

Table of contents

Create Chip Design

Chip Creator™ is a Graphical User Interface (GUI) tool. This GUI tool helps Integrated Circuit (IC) Designers create IC chips hassle-free.

Learn to create new chip design using Chip Creator™.

Prerequisites

A valid Chip Creator™ license.

Open Chip Creator

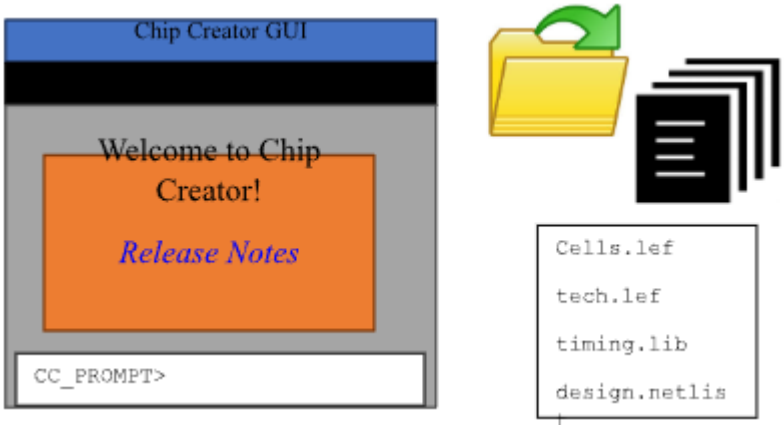
1. Open the Chip Creator tool

2. Enter `start_gui` in the command line to start the Chip Creator tool

3. Open the File menu to open a file

Note: Read the tech.leffile before creating a new design.

Important: Chip Creator™ reuses archived project files, if required.

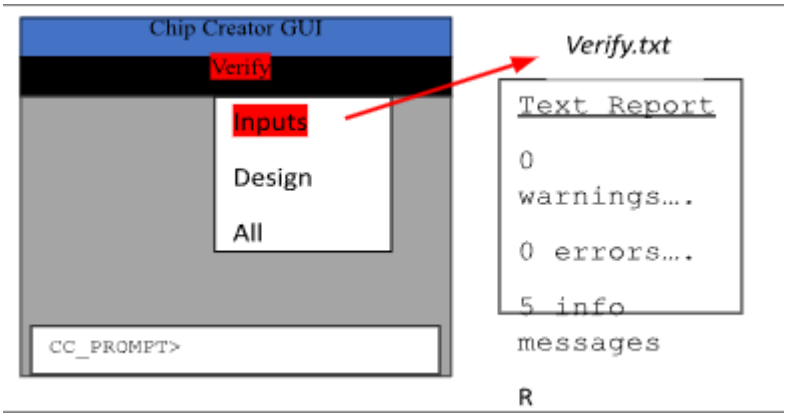


OR

3. Enter `read_file` in the command line to read an existing file

4. On the existing file, go to Verify > Input to check for any error or warning messages prior to making any changes to the file

Note: INFO messages convey the information that this particular file was read in the previous step.



Create Chip Layout

1. On a new file, go to Run > Create to automatically create a chip outline

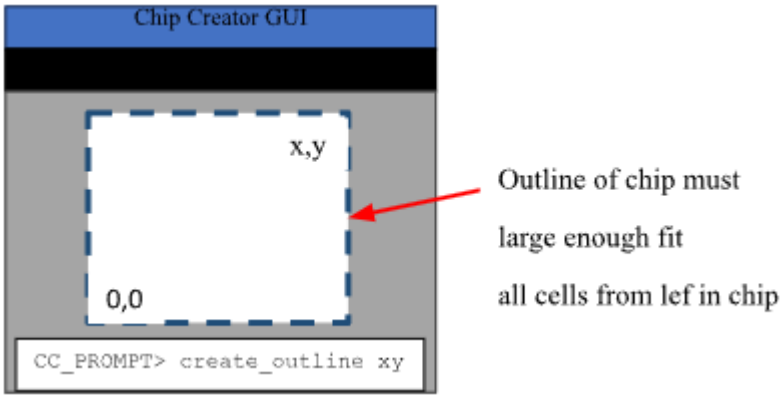
OR

1. Go to Edit > Draw Outline to manually create a chip outline

OR

1. Enter `create_outline xy` in the command line to manually create a chip outline with the known values of x and y

 Note: The unit of measurement for x and y is in microns.



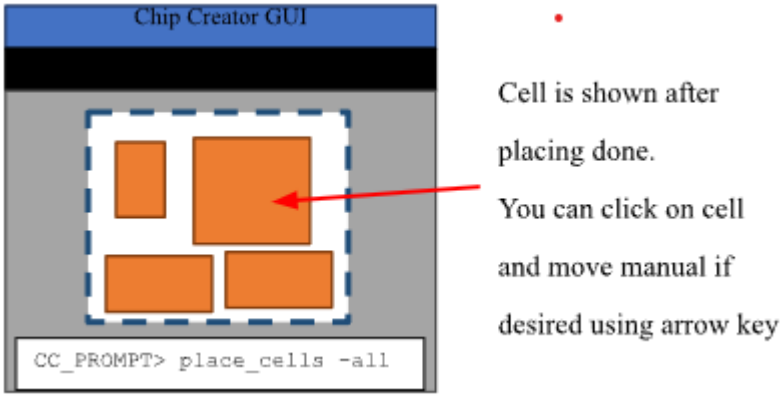
Place Cells

1. Go to Run > Place all cells to put all the cells inside the chip outline

OR

1. Enter `place_cells -all` in the command line to put all the cells inside the chip outline

□ Trivia: Chip Creator™ performs 5 times better than its competitors.



2. Select any individual cell to manually move it to the desired place. [OPTIONAL]

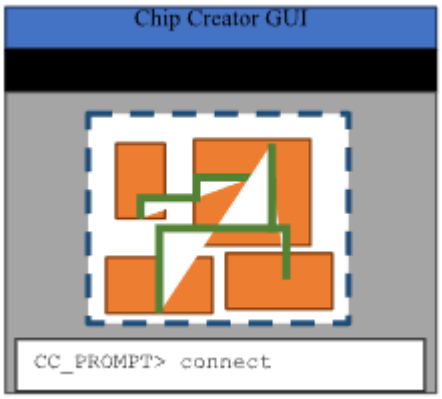
Connect Cells

1. Go to Run > Connect to connect all the cells with the wires

Note: If unable to connect the cells, consider manually moving them a little for the wires to connect properly.

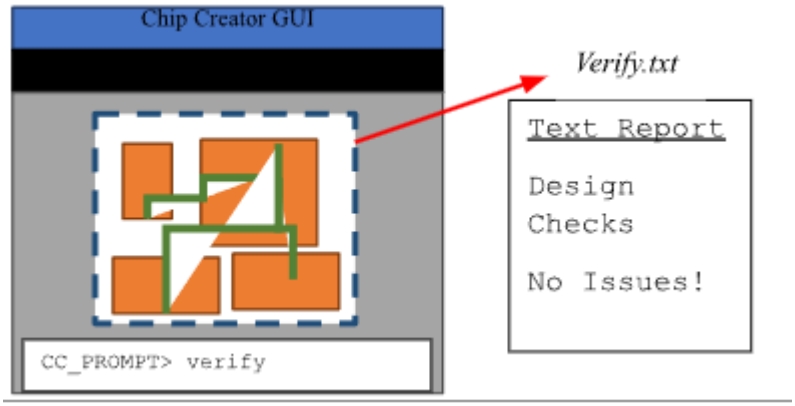
2. Enter `set_layers` in the command line to customize different wire layers

Note: See the tech.lef file to learn more about different wire layers.



Verify Chip Design

1. After properly connecting the cells with different wire layers, go to Verify > All to verify the entire chip design



Result

A new chip design is created and verified.

Index and Glossary

Index

Glossary

Acronyms
IC: Integrated Circuit
GUI: Graphical User Interface.
TM: Trademark

Definitions
Note: Useful tip, can be ignored
Important: Cannot be ignored
Trivia: Additional information

Thank You!