

Chip Creator™

Table of contents

Create Chip Design	3
Prerequisites	3
Open Chip Creator	4
Create Chip Layout	5
Place Cells	6
Connect Cells	7
Verify Chip Design	8
Result	9
Index and Glossary	10

Create Chip Design

Chip Creator™ is a Graphical User Interface (GUI) tool. This GUI tool helps Integrated Circuit (IC) Designers create IC chips hassle-free.

Learn to create new chip design using Chip Creator™.

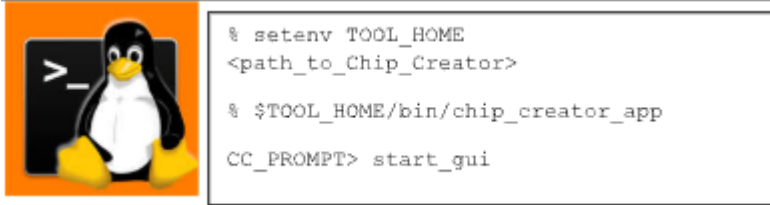
Created with the Personal Edition of HelpNDoc: [Make your documentation accessible on any device with HelpNDoc](#)

Prerequisites

A valid Chip Creator™ license.

Open Chip Creator

1. Open the Chip Creator tool

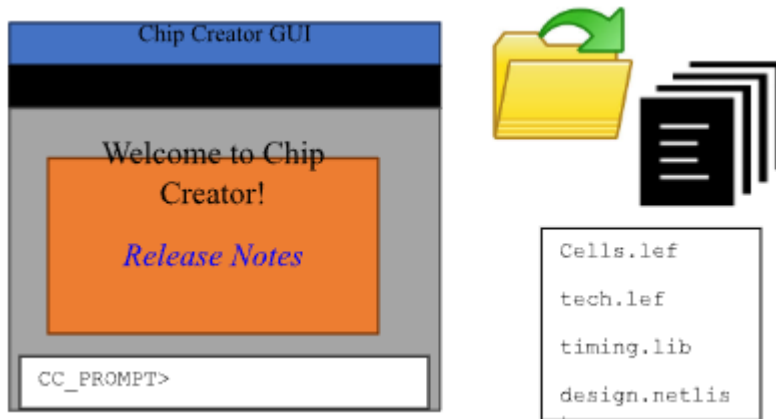


2. Enter `start_gui` in the command line to start the Chip Creator tool

3. Open the File menu to open a file

Note: Read the tech.lef file before creating a new design.

Important: Chip Creator™ reuses archived project files, if required.

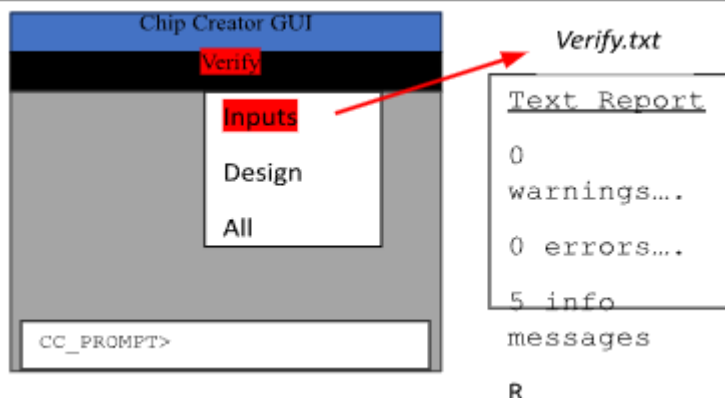


OR

3. Enter `read_file` in the command line to read an existing file

4. On the existing file, go to Verify > Input to check for any error or warning messages prior to making any changes to the file

Note: INFO messages convey the information that this particular file was read in the previous step.



Create Chip Layout

1. On a new file, go to Run > Create to automatically create a chip outline

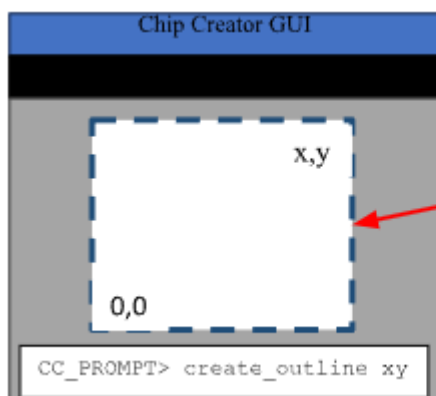
OR

1. Go to Edit > Draw Outline to manually create a chip outline

OR

1. Enter ``create_outline xy`` in the command line to manually create a chip outline with the known values of x and y

□ **Note:** The unit of measurement for x and y is in microns.



Outline of chip must
large enough fit
all cells from left in chip

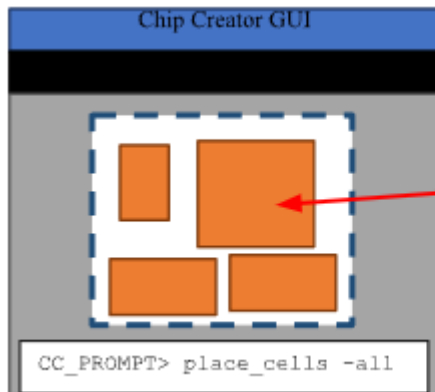
Place Cells

1. Go to Run > Place all cells to put all the cells inside the chip outline

OR

1. Enter ``place_cells -all`` in the command line to put all the cells inside the chip outline

☐ Trivia: Chip Creator™ performs 5 times better than its competitors.



•
Cell is shown after
placing done.

You can click on cell
and move manual if
desired using arrow key

2. Select any individual cell to manually move it to the desired place. [OPTIONAL]

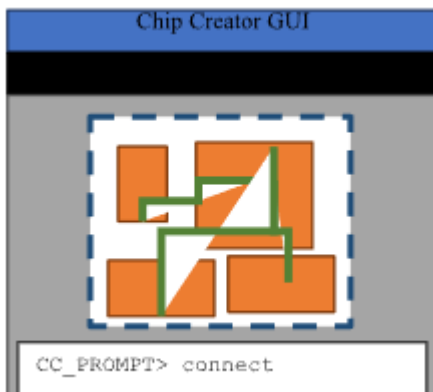
Connect Cells

1. Go to Run > Connect to connect all the cells with the wires

Note: If unable to connect the cells, consider manually moving them a little for the wires to connect properly.

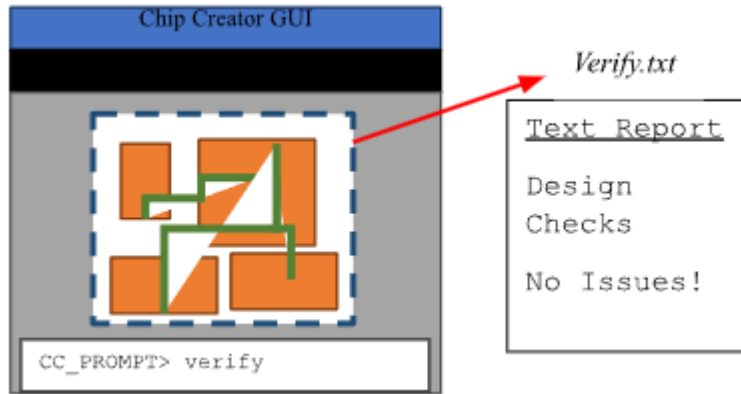
2. Enter `set_layers` in the command line to customize different wire layers

Note: See the tech.lefile to learn more about different wire layers.



Verify Chip Design

1. After properly connecting the cells with different wire layers, go to Verify > All to verify the entire chip design



Created with the Personal Edition of HelpNDoc: [Easily convert your WinHelp HLP help files to CHM with HelpNDoc's step-by-step guide](#)

Result

A new chip design is created and verified.

Created with the Personal Edition of HelpNDoc: [Revolutionize Your Documentation Review with HelpNDoc's Project Analyzer](#)

Index and Glossary

Index

Opening Chip Creator Tool: [Open Chip Creator](#)
Creating Chip Border: [Create Chip Layout](#)
Adding Cells Inside Chip Border: [Place Cells](#)
Connecting Cells: [Connect Cells](#)
Verifying Final Chip Design: [Verify Chip Design](#)

Glossary

Acronyms

IC: Integrated Circuit
GUI: Graphical User Interface.
TM: Trademark

Definitions

Note: Useful tip, can be ignored
Important: Cannot be ignored
Trivia: Additional information

Thank You!

Created with the Personal Edition of HelpNDoc: [Free PDF documentation generator](#)
